Š -Srr <u>ح|</u> -Sb! CONTROL RECORDING PATTERN Sct GENERAT ING SECTION Spl Sct TIMING CREATING SECTION Scp Spt RECORDING WAVEFORM CREATING SECTION Sadr Sp -24 Stm Ssyc BOTTOM DETECTING SECTION ∞-PEAK DETECTING SECTION 9 (T) RECORDING POWER SETTING SECTION RF DETECTING SECTION ~Spc PRE-FORMAT DETECTING SECTION DRIVING SECTION 23-~pos 2 -Spp **DRIVER** <u>0</u>, Srf φ 1:DVD-R **PICKUP**

.

F1G. 2

LEAD-OUT AREA DATA AREA - DA -20-2 | SECTOR LEAD-IN AREA <u>.</u> C·H 20-3 1,2 RIMA (720-(n-1))8-PCA 20-n

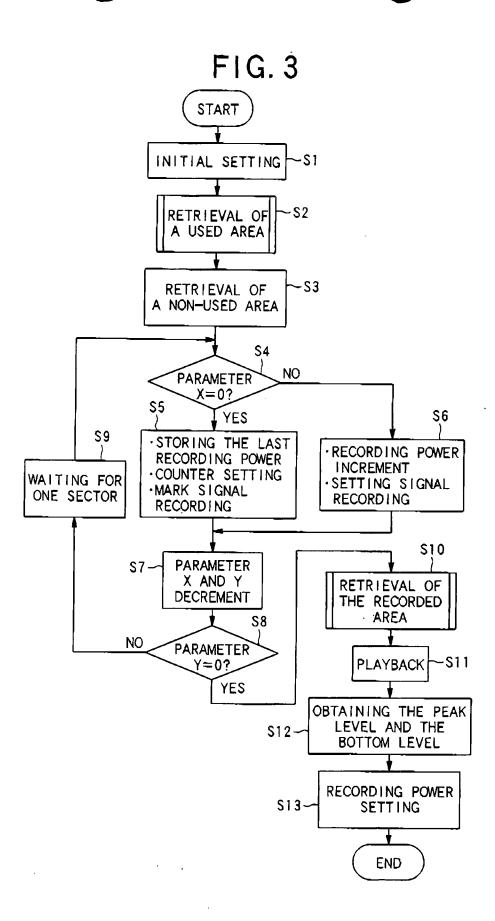


FIG. 4

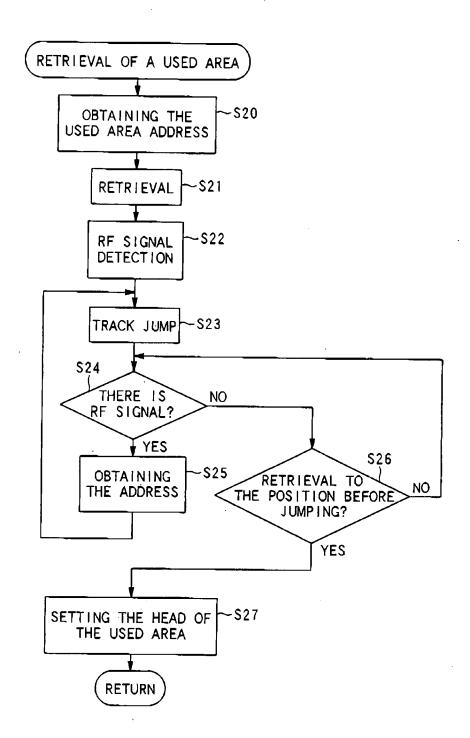


FIG. 5

